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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
NOTICE OF APPEAL FROM THE PRIMARY EXAMINER  
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re application of:

MARK A. BURNS

Serial No.: 10/026,053 (TI-33380)

Filed: December 21, 2001

For: PARALLEL INTEGRATED CIRCUIT TEST APPARAT US AND TEST METHOD

Group Art Unit: 2829

Examiner Jimmy Nguyen

Director of the United States  
Patent and Trademark Office.  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby appeals to the Board of Appeals from the decision dated December 23, 2003 of the Primary Examiner finally rejecting claims 1 to 33, all of the rejected claims.

The item(s) checked below are appropriate:

1. xx An extension of time to respond to the final rejection  
\_\_\_\_\_ was granted on  
xx is requested for one month(s).
2. x A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
3. xx Fee \$330.00:  
\_\_\_\_\_ Not required (*Fee paid in prior appeal*)  
x The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Account No. 20-0668. A duplicate copy of this sheet is enclosed.

Signature [Rule 191(b)]

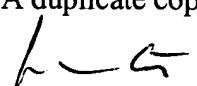
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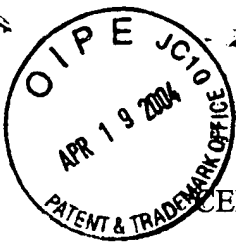
(to which correspondence is to be sent)

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330.00 DA

  
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CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8 (a)

I certify that the attached document as listed below is being deposited with the United States Postal Services as First Class Mail in an envelope addressed to the Director of the United States Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

RCE

4-14-04

Jay M. Cantor

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